



VFD PROJECT SUBMITTAL

TO:
ATTN:

Date: 11/12/2025

Quote #:
Job Name:

Submittal Drawings Enclosed

Submittal Schedule Details

Details, Features and Reference Drawings	HV35U2021ABA
Horsepower (HP)	7.5
Voltage (V)	240
Amperage (A)	21
Quantity	1
Enclosure	IP20
Input Impedance (%)	N/A
SCCR (kA)	100*
Dimensions (H" x W" x D")	5.04 x 5.51 x 5.63
Weight (lbs)	4.4
Specification	SG.HV305.10QW
Print Size	8.5" x 11.0"
Submittal Page	4
Electrical Schematic	DS.HV305.01
Print Size	8.5" x 11.0"
Submittal Page	7
Wiring Requirements	See Outline Drawing
Print Size	-
Submittal Page	8
Outline Drawing	DD.HV305.FR3.1.IP20
Print Size	11.0" x 8.5"
Submittal Page	8

* Capable of the specified symmetrical amperes shown in the SCCR field with sufficient branch circuit short circuit protection installed as specified by applicable codes.



Table of Contents

Yaskawa Submittal Cover Sheet	Page 1
Submittal Schedule Details	Page 2
Table of Contents	Page 3
Specification: SG.HV305.10QW	Page 4
Electrical Schematic: DS.HV305.01	Page 7
Outline Drawing: DD.HV305.FR3.1.IP20	Page 8

YASKAWA

Variable Frequency Drive (VFD) HV305 Mechanical Specification Submittal

GENERAL

The HV305 is a high-performance PWM (pulse-width-modulated) AC drive. Three-phase input line power is converted to a sine-coded, variable-frequency output that provides optimum speed control for any conventional squirrel-cage induction motor, permanent-magnet motor, or synchronous reluctance motor. The use of IGBTs (Insulated Gate Bipolar Transistors) with a carrier frequency range of 2-15 kHz enables quiet motor operation.

This drive has one control logic board for all horsepower ratings. Printed circuit boards employ surface-mount technology, providing both high reliability and a small physical footprint for printed circuit assemblies. The microprocessor delivers the computing power necessary for complete three-phase motor control in building automation systems.

Operating Principle: The input three-phase AC line voltage is first rectified to a fixed DC voltage. Using pulse-width modulation (PWM) inverter technology, the DC voltage is processed to produce an output waveform consisting of a series of variable-width pulses. Unique firmware algorithms optimize motor magnetization by controlling voltage, current, and frequency to generate a nearly sinusoidal output waveform.

STANDARDS

- UL 61800-5-1 (Power Conversion)
- CE mark 2006/42/EC MD
- CE mark 2014/35/EU LVD
- CE mark 2014/30/EU EMC
- CE mark 2011/65/EU RoHS
- EN 61800-3
- EN 61800-5-1 (LVD)
- EN 62061 (SIL CL3)
- EN ISO 13849-1 (Cat 3, PLe)
- EN 61800-5-2 (SIL3)
- EN 61800-6-2
- EN 50581
- CSA B44.1/ASME A17.5
- UL, cUL listed; CE, CSA, TUV, RCM marked
- UL and CSA Compliance for A2L Refrigerants (UL-60335-2-40, LZGH2)

ENVIRONMENTAL & SERVICE CONDITIONS

Ambient service temperature:

-10°C to 50°C, 60°C maximum with derate

Ambient storage temperature IP20/Protected Chassis:

-20°C to 70°C

Humidity: 0% to 95%, non-condensing

Altitude: to 1,000 meters; 4,000 meters with derate

Service factor: 1.0

Vibration: 10 Hz to 20 Hz: 1 G (9.8 m/s², 32.15 ft/s²)

20 Hz to 55 Hz: 0.6 G (5.9 m/s², 19.36 ft/s²)

RoHS 2 Compliant

WEEE Directive

QUALITY ASSURANCE

In-circuit testing of all printed circuit boards is conducted to ensure proper manufacturing.

Final printed circuit board assemblies are functionally tested via computerized test equipment.

All fully assembled controls are computer-tested with induction-motor loads to ensure compliance with unit specifications.

The average MTBF (Mean Time Between Failure) is 28 years.

CONSTRUCTION

VFD power input stage converts three-phase AC line power into a fixed DC voltage via a solid-state full-wave diode rectifier with MOV (Metal Oxide Varistor) surge protection.

Intermediate Section of the VFD - DC bus maintains a fixed DC voltage with filtering and short circuit protection as a DC supply to the VFD output section. It interfaces with the VFD diagnostic logic circuit to continuously monitor and protect the power components.

Output Section of the VFD - Insulated Gate Bipolar Transistors (IGBTs) convert the DC bus voltage to a variable frequency and voltage, utilizing a PWM-sine-coded output to the motor. Motor noise at 60 Hz is less than 2 dB above the motor noise from across-the-line operation when measured at a distance of one meter.

POWER AND CONTROL ELECTRONIC HOUSINGS

IP20/Protected Chassis enclosure: 240 V single-phase, 1/6 thru 5 HP; 240 V three-phase, 1/6 thru 30 HP; 480 V three-phase, 1/2 thru 30 HP

IP20/UL Type 1 wall-mounted enclosure (optional kit required): 240 V single-phase, 1/6 thru 5 HP; 240 V three-phase, 1/6 thru 30 HP; 480 V three-phase, 1/2 thru 30 HP

Microprocessor-based control circuit

Non-volatile memory (EEPROM): all program memory is retained when the VFD is disconnected from power.

Digital operator keypad and display provide local control and readout capability:

- Local/Remote/Start/Stop commands
- Speed Reference command
- Reset command

Easy-to-remove heatsink cooling fan with programmable on/off control.

USB mini-B port for quick and easy PC connection

PROTECTION

Output current overload rating of 110% for 60 seconds (normal duty) or 150% for 60 seconds (heavy duty)

Output short circuit protection

Current limited stall prevention (overload trip prevention) during acceleration, deceleration, and run conditions

Optically isolated operator controls

Fault display

“Hunting” prevention logic

Electronic ground fault protection

Electronic motor overload protection (UL approved)

DC bus charge indication

Heatsink overtemperature protection

Cooling fan operating hours recorded

Input/output phase loss protection

Reverse prohibit selectability

Suitable for use on a circuit capable of delivering not more than 100 kA RMS symmetrical amperes, 240 V or 480 V maximum, when protected by specified devices

OPERATION

The output frequency and speed display can be programmed for other speed-related and control indications, including Hz, RPM, % of maximum RPM, or custom.

Power loss ride-through (2 seconds capable)

VFD accepts either a direct-acting or a reverse-acting speed command signal.

Bi-directional “Speed Search” capability to start into a rotating load. Two types: current detection and residual voltage detection

DC injection braking

Remote Run/Stop command input

One programmable 0 to 10 VDC or 4-20 mA analog output, proportional to drive monitor functions, including output frequency, output current, output power, PI feedback, output voltage, and others

A standard 5-character, 7-segment Local/Remote LED display, or an optional 8-line, 32-character Local/Remote LCD display, provides readouts for output frequency, output voltage, output current, output power, DC bus voltage, interface terminal status, PI feedback, and fault status.

Over 100 programmable functions, resettable to factory presets

User parameter initialization to re-establish project-specific parameters

Ramp-to-stop or coast-to-stop selection

Auto restart capability: 0 to 10 attempts with adjustable delay time between attempts

One custom selectable Volts/Hertz pattern and multiple preset Volts/Hertz patterns

Auto speed reference input signal, adjustable for bias and gain

While the VFD is running, operational changes in control and display functions are possible, including:

- Acceleration time (0 to 6000 seconds)
- Deceleration time (0 to 6000 seconds)
- Frequency reference command
- Monitor display
- Removable digital operator

Automatic energy saving, reduced voltage operation

PRODUCT FEATURES

Displacement power factor of .98 throughout the motor speed range

Data logging – record status for up to 10 monitors with adjustable sample time, with optional LCD keypad

Optional LCD keypad with built-in real-time clock for time and date stamping events, along with timer functions for starting, stopping, and speed changes without the need for external controls

Voltmeter, ammeter, kilowatt meter, elapsed run time meter, and heatsink temperature monitoring functions

24 VDC, 150 mA transmitter power supply

Input and output terminal status indication

Diagnostic fault indication

VFD efficiency: 96% at half-speed; 98% at full-speed

“S-curve” soft start / soft stop capability

Run/Fault output contacts

Serial communication loss detection and selectable response strategy

“Up/Down” floating-point control capability

Output Frequency 0 to 590 Hz

Controlled speed range of 40:1 (V/f, V/f with encoder), 200:1 (open loop vector), 200:1 (advanced open loop vector), 20:1 (open loop vector for PM), 100:1 (advanced open loop vector for PM, EZ vector)

Maximum output frequency: 590 Hz

Safe Torque Off: SIL3, PLe

200% starting torque capability, available from 0 Hz to 60 Hz

Remote speed reference (speed command) signal:

- 0 to 10 VDC (20 k Ω)
- 4 to 20 mA DC (250 Ω)

Critical frequency rejection capability: three selectable, adjustable bandwidths

Analog/Digital Virtual I/O – internally sends an output to an input (no wiring needed)

Adjustable carrier frequency, from 2 kHz to 15 kHz

Dynamic noise control for quiet motor operation

Programmable security code

Cloud service (Yaskawa Drive Cloud) for product registration and parameter storage

Store one parameter set in a standard LED keypad or up to four additional parameter sets in an optional LCD keypad

Integrated PLC (DriveWorks EZ)

Seven programmable multi-function input terminals (24 VDC) providing 60+ programmable features, including:

- Fault reset
- Motor-operated pot (MOP)
- External fault
- 16 preset speeds
- PI control enable / disable

Three programmable multi-function digital output (qty 1 Form C rated 1 A @ 250 VAC & 30 VDC, qty 2 Photocoupler rated 50 mA @ 48 VDC), providing 50+ functions, including:

- Fault status
- Run status
- Overtorque / undertorque detection
- Serial communication status

Sixteen preset speeds

Built-in BACnet MS/TP and Modbus RTU protocols are accessible via standard RS-422/485 communication.

BACnet/IP, EtherNet/IP, Modbus TCP/IP, PROFINET, EtherCAT, PROFIBUS-DP, and DeviceNet are optionally available.

Rotational as well as Stationary motor auto-tuning

A Kinetic Energy Braking (KEB) function stops the motor up to twice as quickly as without it.

Control Methods Include:

- V/f Control
- V/f Control with encoder feedback
- Open-loop vector
- Advanced open-loop vector
- Open-loop vector for PM
- Advanced open-loop vector for PM
- SynRM Motor Control

Motor Types:

- Induction
- Permanent Magnet
- Synchronous Reluctance

Temperature-controlled fans

Side-by-side mounting

Standard LED or Optional LCD keypad with Local/Remote, Start/Stop, and Copy keypad functions.

Motor preheat function

Flash upgradeable firmware

Heatsink overtemperature speed fold-back feature

Fan failure detection and selectable drive action

Programming and firmware upgrade without three-phase main power via DriveWizard Mobile

Programming Application

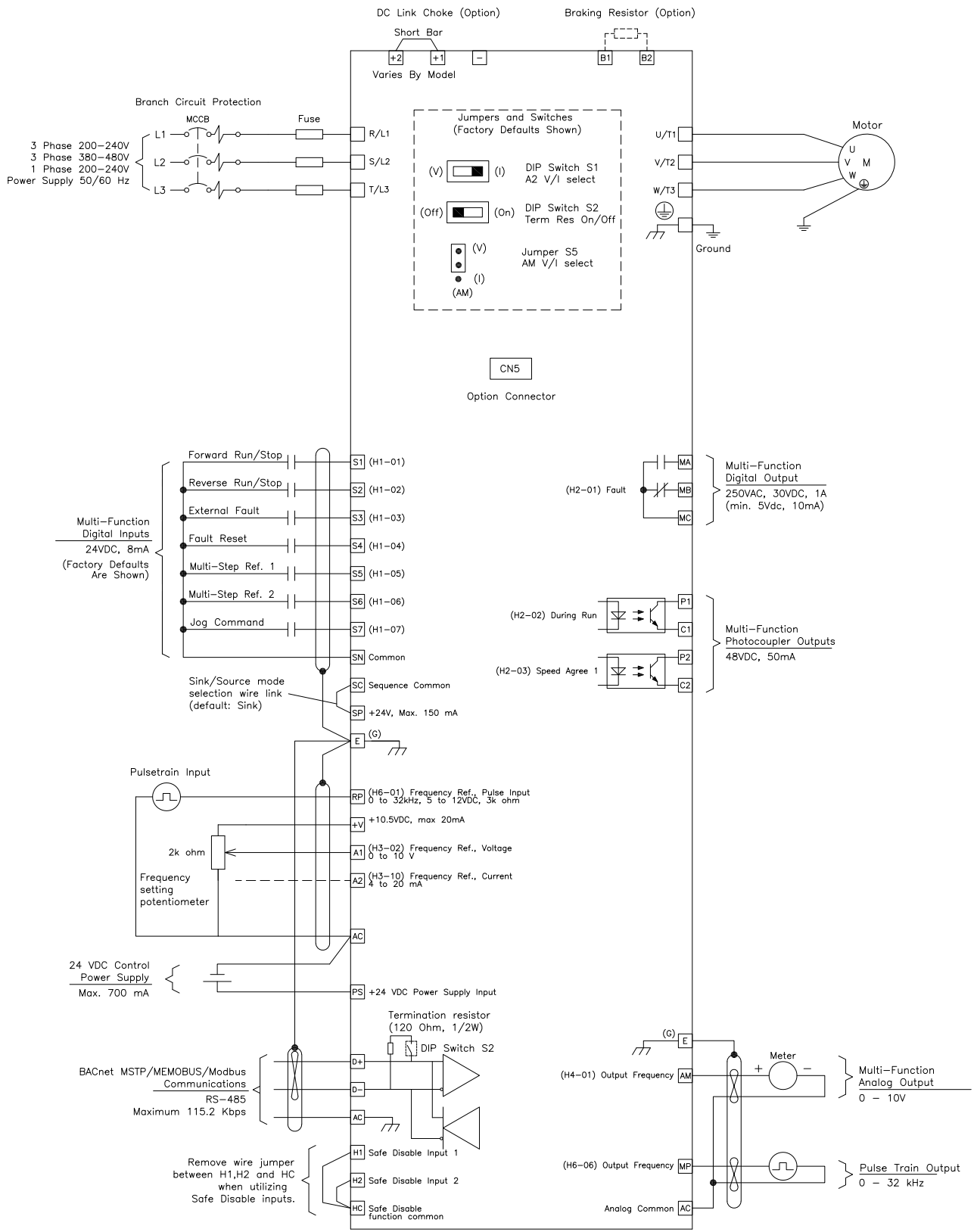
LED Status Ring

Conformal coating (IEC 60721-3-3, IP20/Protected Chassis: 3C2, 3S2)

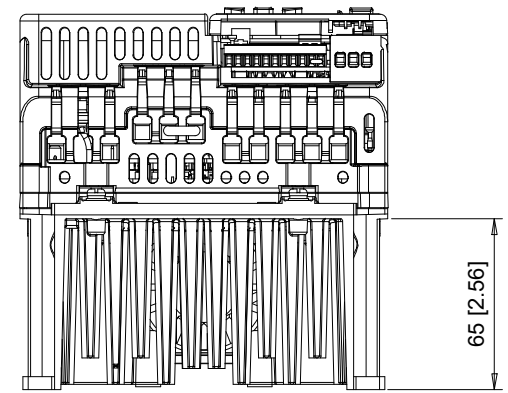
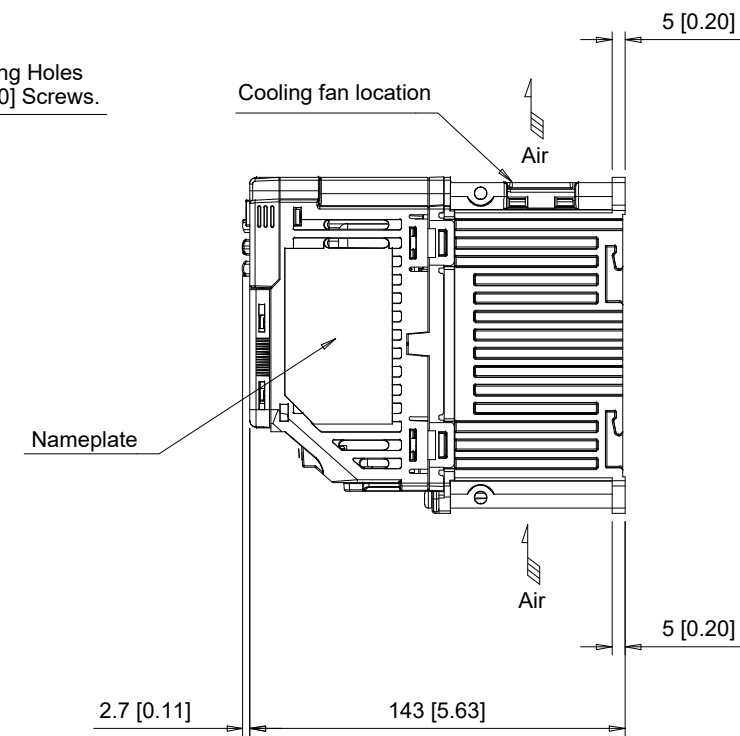
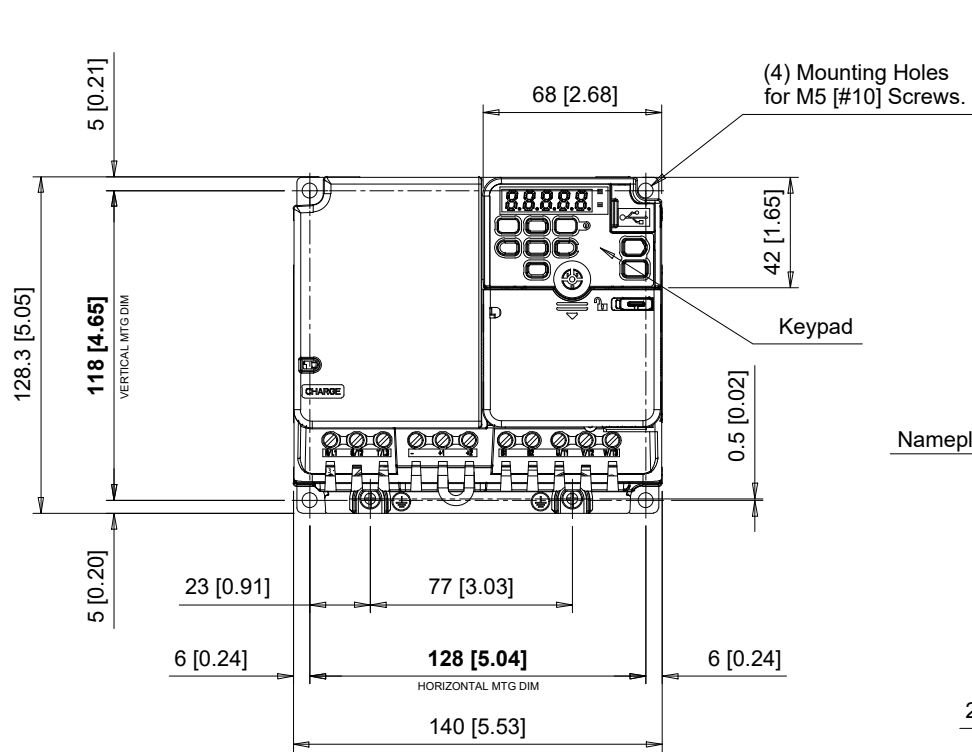
SG.HV305.10QW

A
B
C
D
E
F
G
H
I
J

HV305



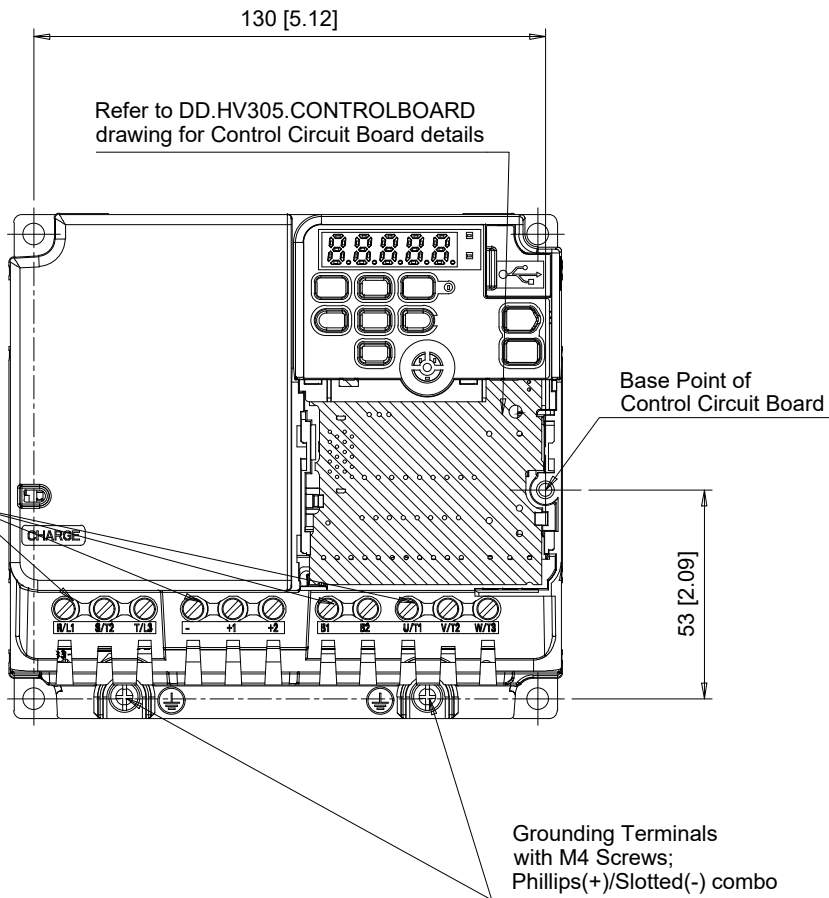
Note: Refer to technical manual for further details.



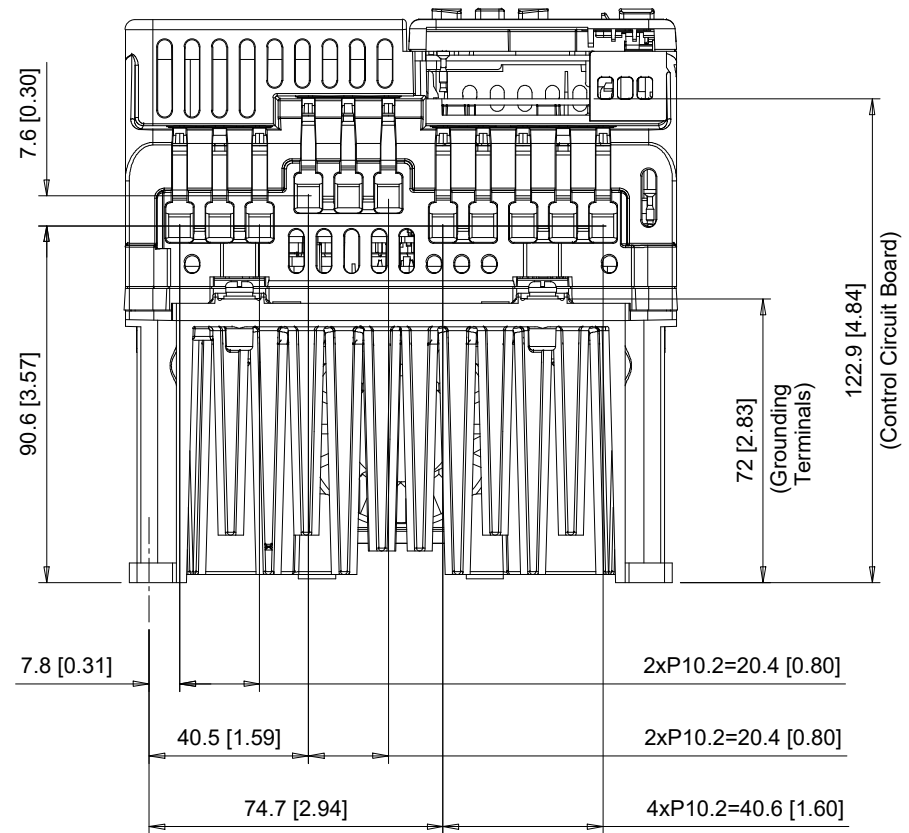
C/C: HV35□	INPUT VOLTAGE (VAC)	WEIGHT Kg (lb)	COOLING FANS
2021□□□	200 TO 240 3-PHASE	2.0 (4.4)	1
4012□□□	380 TO 480 3-PHASE	2.0 (4.4)	1

□ denotes A-Z

FRONT VIEW



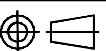
BOTTOM VIEW



C/C: HV35□	TERMINAL SYMBOL	WIRE RANGE AWG (mm ²)	TERMINAL SCREW	CLAMPING TORQUE N.m. (lb.in.)
2021□□□ 4012□□□	R/L1, S/L2, T/L3	14 - 6 (2.5 - 16)	M4	1.7 (15)
	U/T1, V/T2, W/T3	14 - 6 (2.5 - 16)	M4	1.7 (15)
	-, +1, +2	14 - 6 (2.5 - 16)	M4	1.7 (15)
	B1, B2	14 - 6 (2.5 - 16)	M4	1.7 (15)
	⊕	14 - 10 (2.5 - 6)	M4	1.2 - 1.5 (11 - 13)

□ Denotes A-Z
Note: Refer to applicable codes and standards for proper wire type and size.

YASKAWA


UNITS MM [IN]
SCALE 1:2

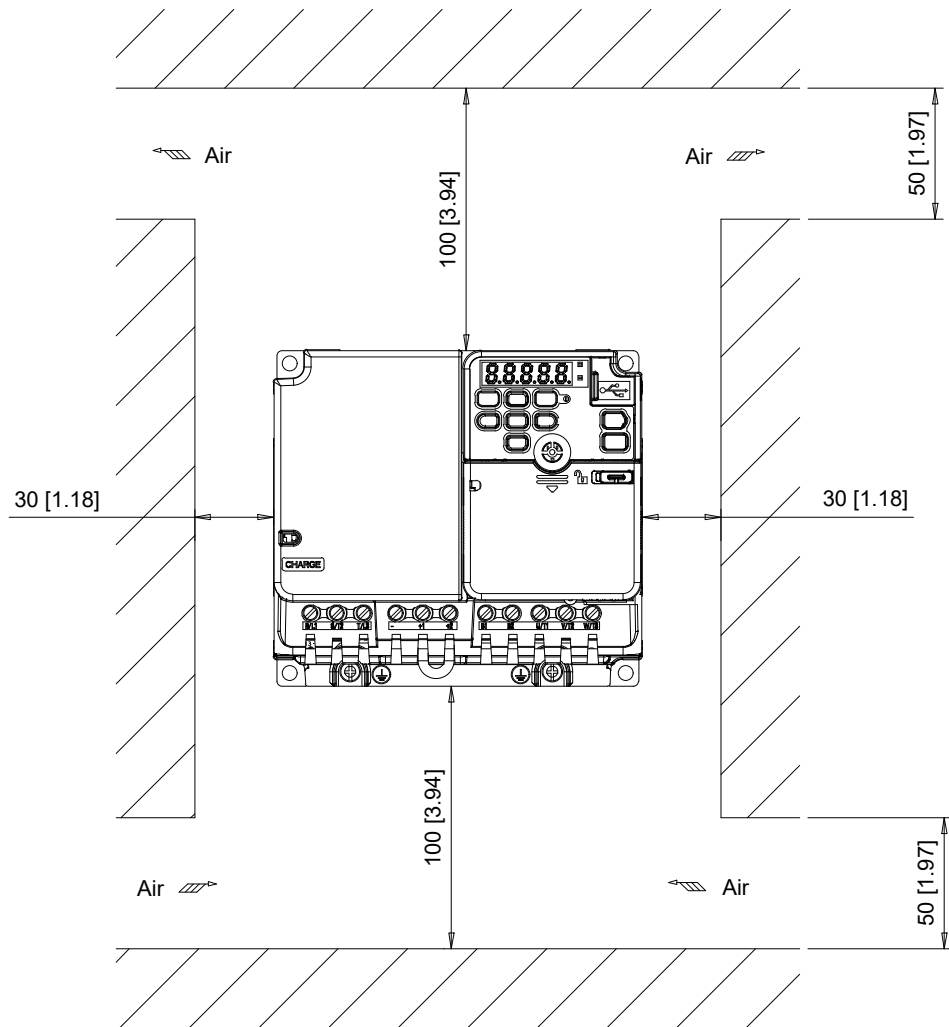
DRWN.	J. MATTAS	9-20-22
CHKD.	J. PIOTROWSKI	9-21-22
TECH.	L. UDDIN	9-27-22
APRV.	J. CAIRO	9-27-22

TITLE
HV305 DIMENSION DRAWING
FRAME SIZE 3.1
IP20 ENCLOSURE

SIZE A	PAGE 2 OF 3
DWG.NO.	DD.HV305.FR3.1.IP20

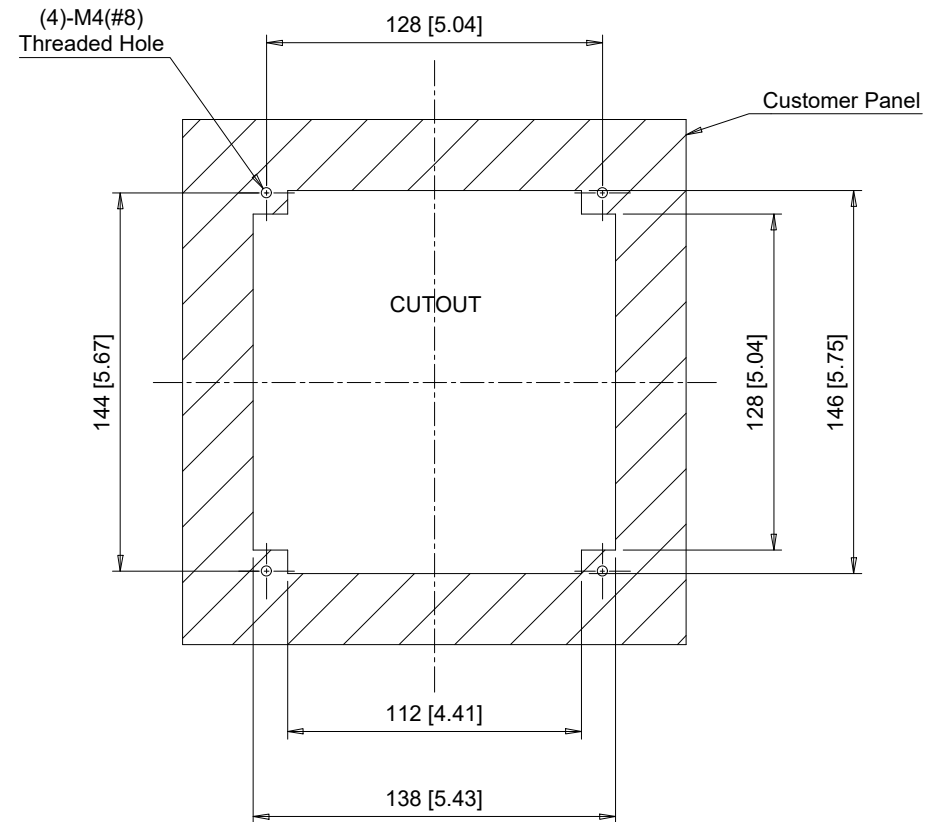
REV
0

Minimum Installation Clearances



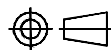
Note:
For alternate mounting configurations, consult manual.

Panel Cutout For External Heatsink Mounting Configuration



Notes:
Exposed heatsink minimum clearance = 70 [2.76].
Refer to external heatsink instructions document for additional details.

YASKAWA



UNITS MM [IN]

SCALE 1:3

DRWN.	J. MATTAS	9-20-22
CHKD.	J. PIOTROWSKI	9-21-22
TECH.	L. UDDIN	9-27-22
APRV.	J. CAIRO	9-27-22

TITLE
HV305 DIMENSION DRAWING
FRAME SIZE 3.1
IP20 ENCLOSURE

SIZE	A	PAGE	3 OF 3	REV
DWG.NO.	DD.HV305.FR3.1.IP20			0